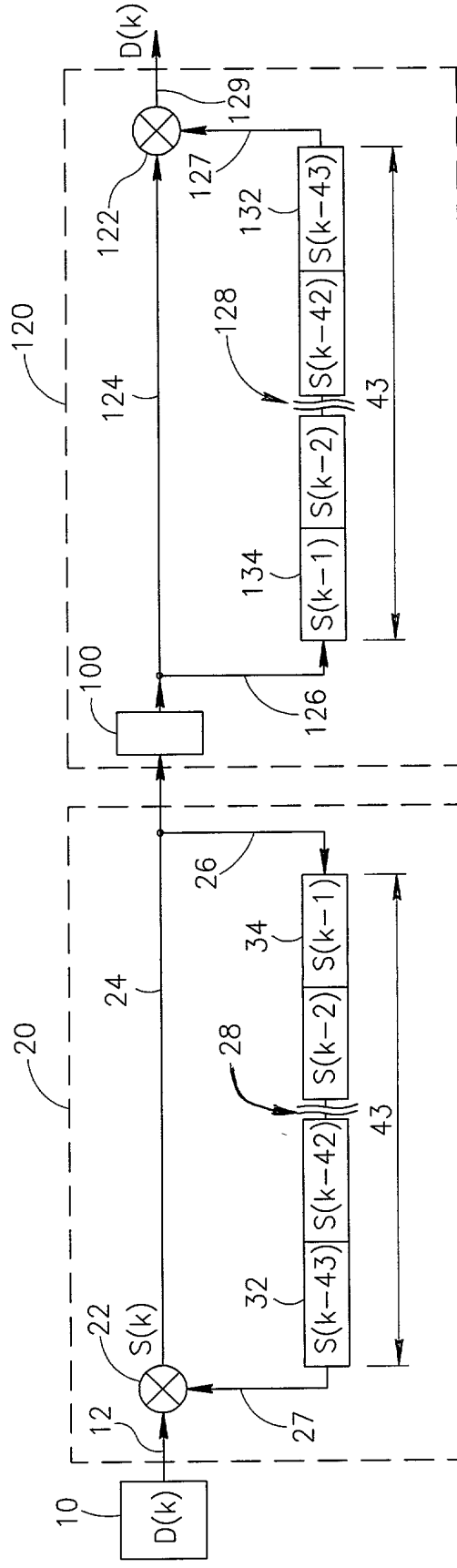


FIG. 1



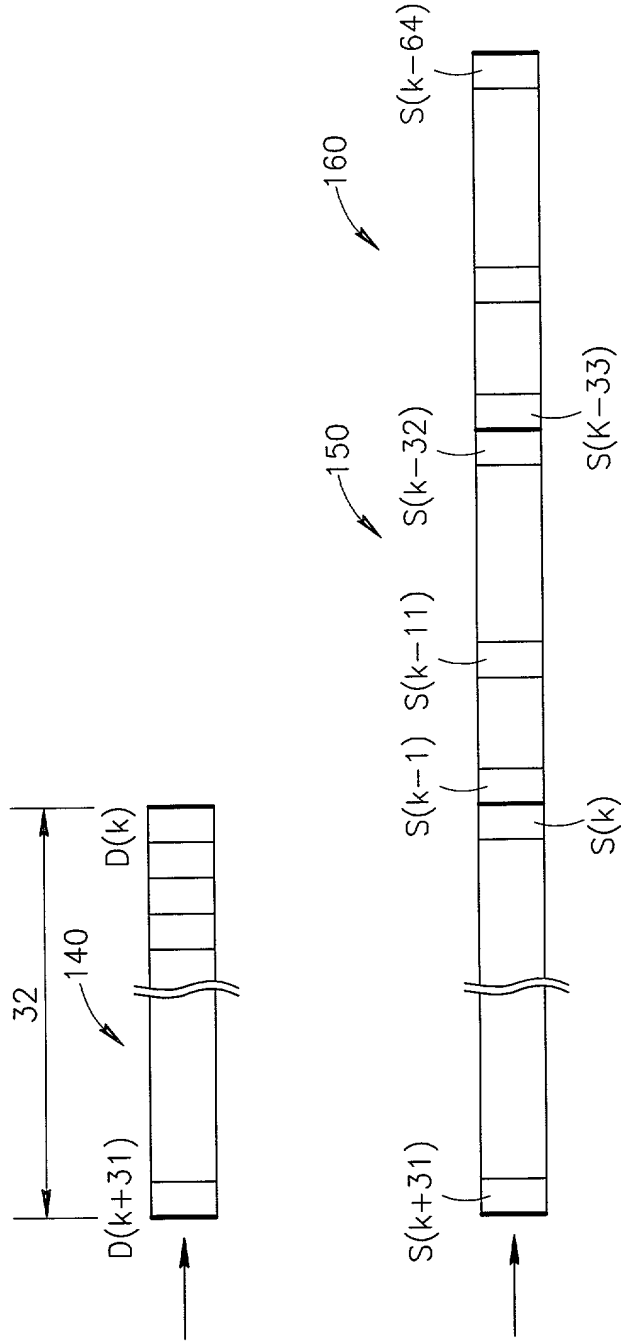
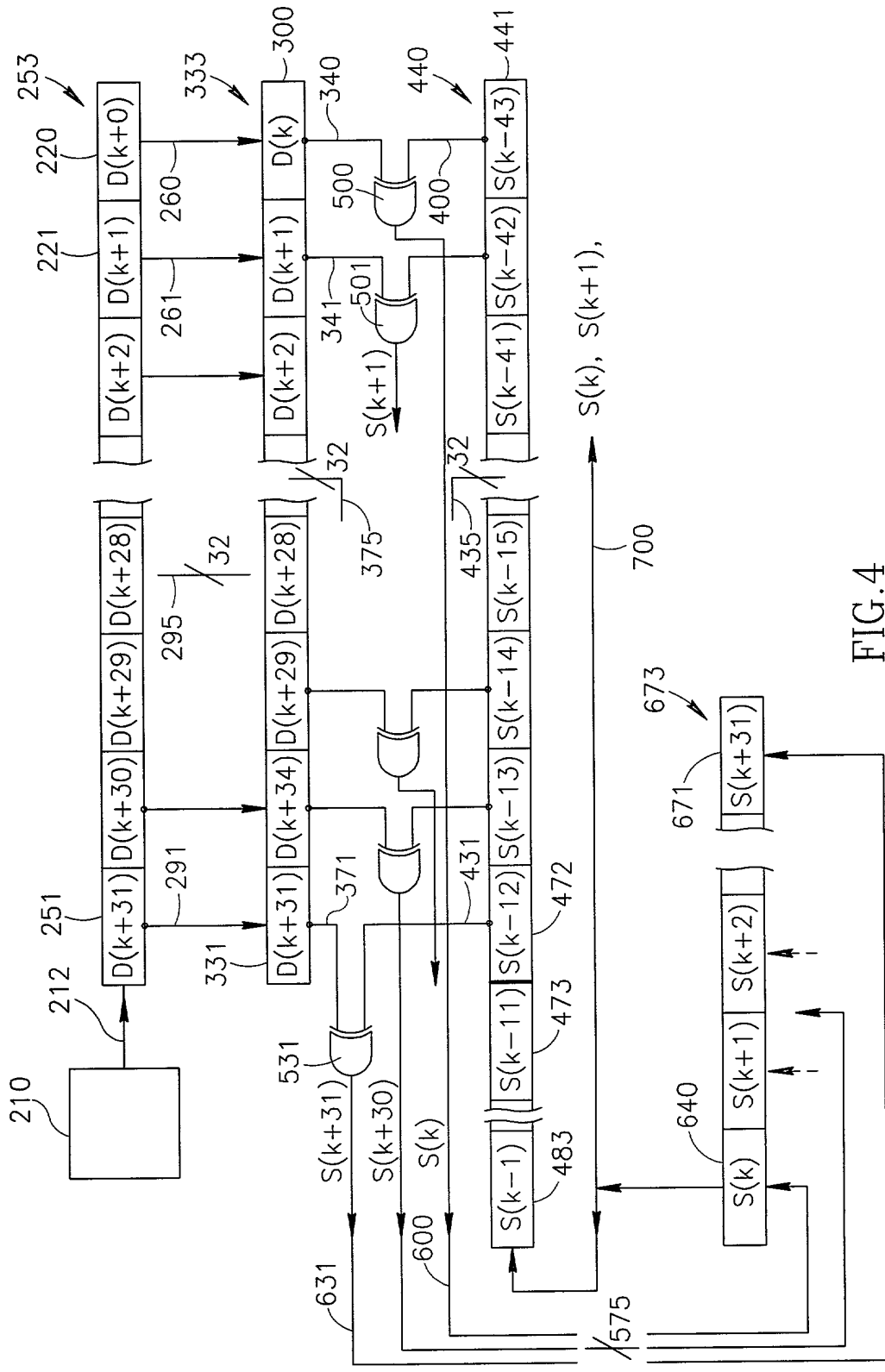


FIG. 3



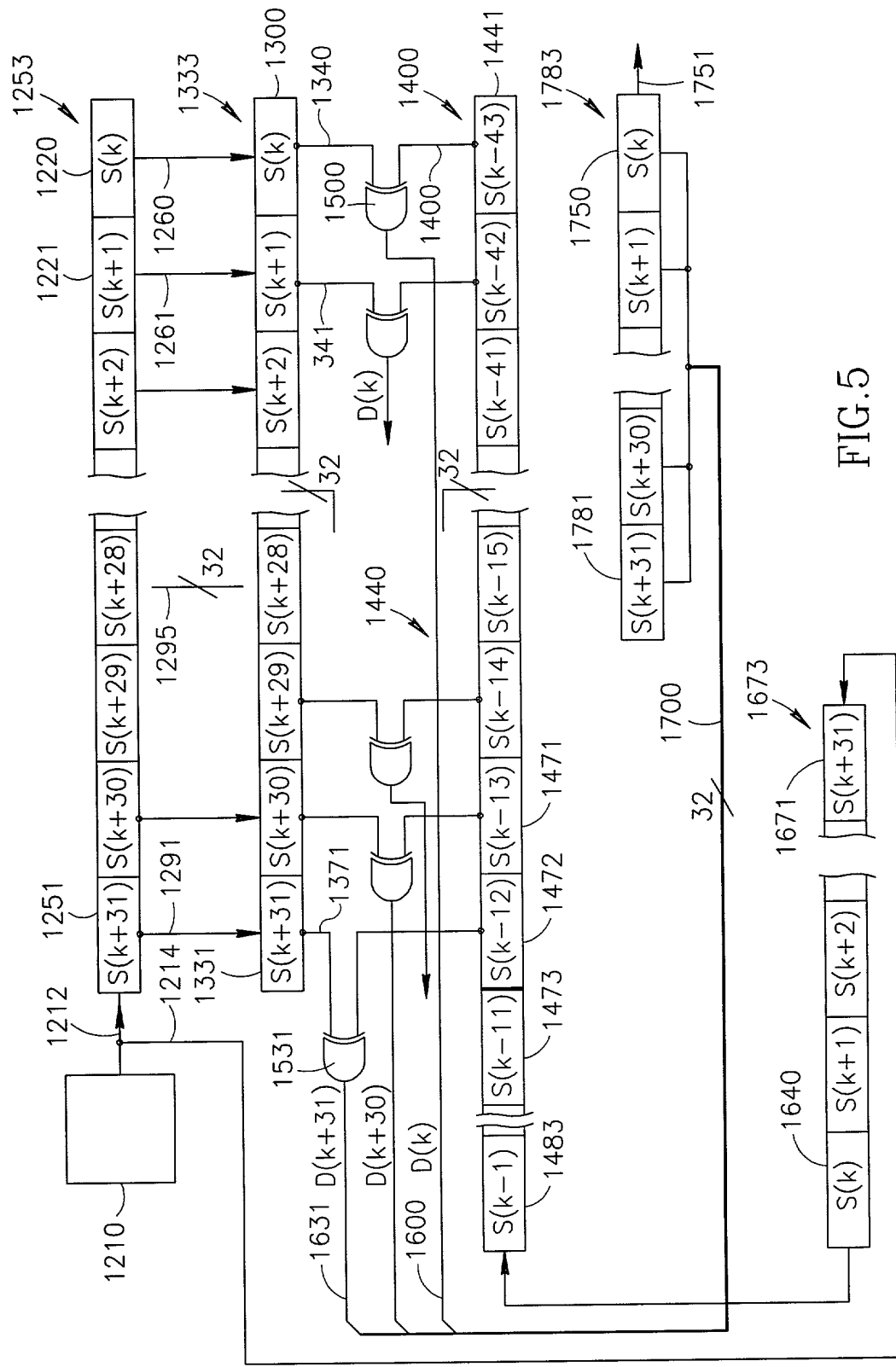
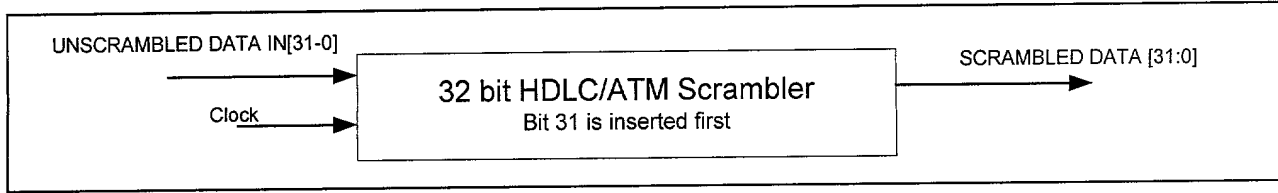


FIG. 5

FIG. 6



32 bit HDLC/ATM Scrambler Implementation

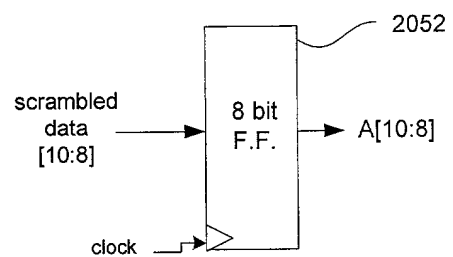
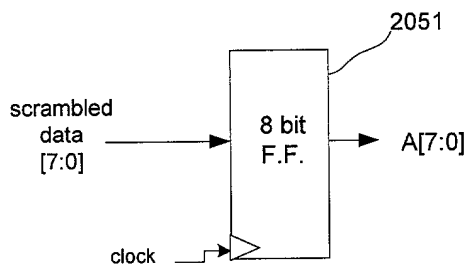
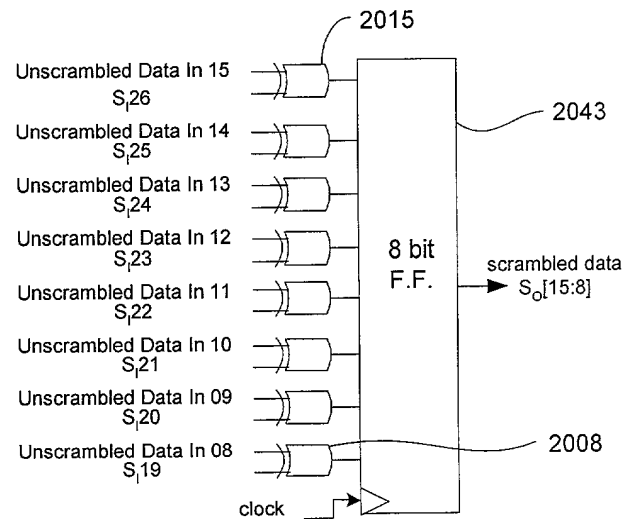
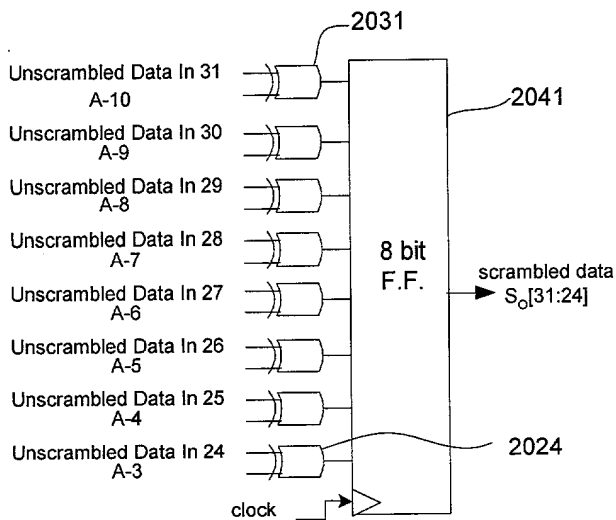
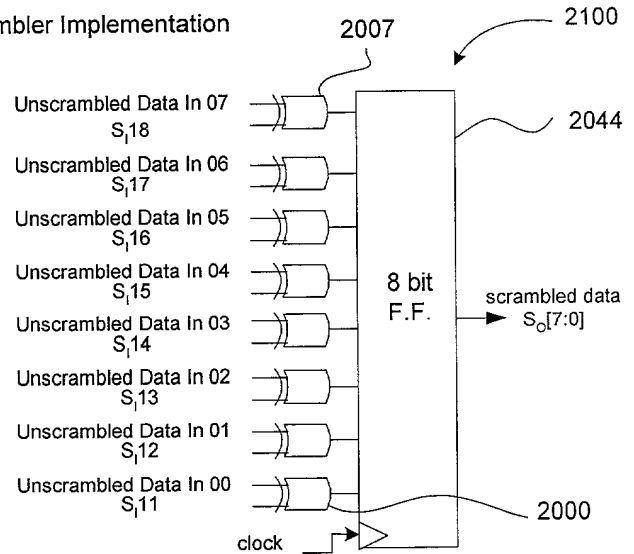
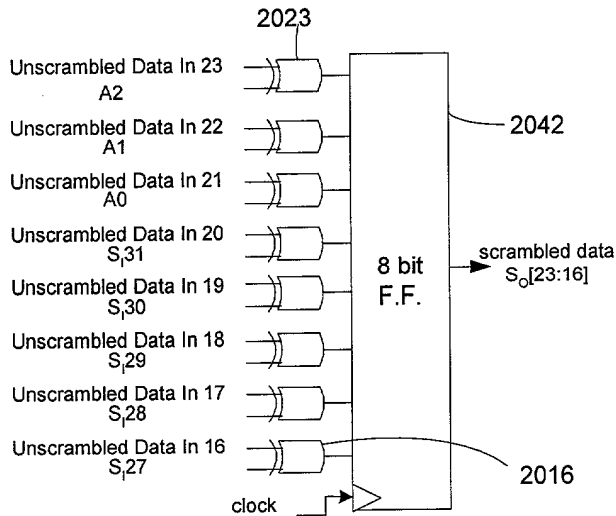
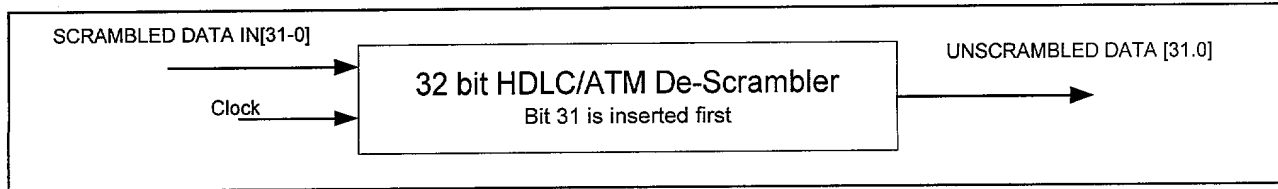


FIG. 7



32 bit HDLC/ATM De-Scrambler Implementation

